

ABSTRACT OF THE DISCLOSURE

In an image processing device, an edge detecting circuit 1 produces edge signals that represent edge components present in image signals, and an adder circuit 2 superimposes the edge signals on the image signals to produce edge-enhanced image signals. The image signals before edge enhancement are fed to a range setting circuit 3 so that, based on the image signals preceding and following a target image signal that is about to be processed by a clipper 4, a range in which the data value of the target image signal is allowed to vary is set. Based on the range of data values thus set by the range setting circuit 3, the clipper 4 clips the data values of the edge-enhanced image data.